

Low level attacks

Shellcode (part 1)

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- Processes are associated with two user ids
 - Real UID: who started the process
 - Effective UID: for who the process acts
- Similarly, there are real and effective group ids
- If SUID is set, effective UID is set to the user owning the file
- If SGID is set, effective GID is set to the group owning the file
- `exec*` functions start new processes... acting for the effective user and group!

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Problems we have to face

- Inject our shellcode in a vulnerable buffer
- Jump to the first instruction of our shellcode

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The NOP Method

<NOPS (0x90)> <shellcode> <padding> <saved return address>

- We will jump in the NOP sled
- The more NOPS, the more likely the injection
- Follow the instructions in `attack-victim.txt`

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- Check the machine code with `objdump`

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08048060 <_start>:  
8048060:    bb 00 00 00 00      mov     ebx,0x0  
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- Now try `exit_shellcode.c`

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`xor eax, eax`
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- Try `exit2.asm` and `exit2_shellcode.c`

- Let's look again `shell.c`
- Possible implementation in assembly: `shell.asm`

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Problems to face

- We cannot use zeros
- We should use relative addressing as much as possible
- Can we get the address of `filename`?

Try `shellcode.asm`

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- It is popped and stored into a register
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Better to extract the shellcode automatically

```
objdump -D -M intel shellcode.o | grep -P ":\t" |  
sed 's/.*:\t//' | sed 's/\s*\t.*$/' |  
sed 's/ /\x/g' | sed 's/\(.*\)"/\x\1/'
```


Now check `shellcode-with-p.asm`

Exercise

Extract the shellcode and inject in `shellcode2.c`

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Example

- Replace the return address with the address of `system()`
- Leave 4 bytes (it is the return address of `system()`)
- Write the address of the string to execute
- Follow the instructions in `ret2libc.txt`
- Also check `bypass-suid-drop-policy.txt`

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Return Oriented Programming (ROP)

Chain several calls to small instruction sets terminated by `ret`

- NX bit: mark each memory segment as writable xor executable
 - Protect against code injection

Protection mechanisms

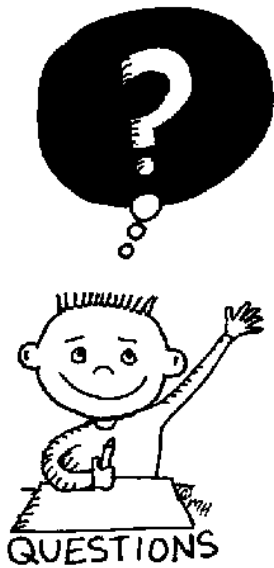
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 - Limit calls in case of overflows
- ASLR: Address Space Layout Randomization
 - Randomly change addresses at each execution



END OF THE
LECTURE