

Workshop ASI
Get Fun with Buffer Overflows

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1 Introduction

- Context and goal
- Overview and example

2 Assembly

- Warm up
- Computer architecture
- Most frequent instructions
- C/C++ calling convention

3 Buffer overflow and shellcode

- Simple examples
- Privilege escalation
- Help yourself with peda
- Return-to-libc and ROP

4 Conclusion

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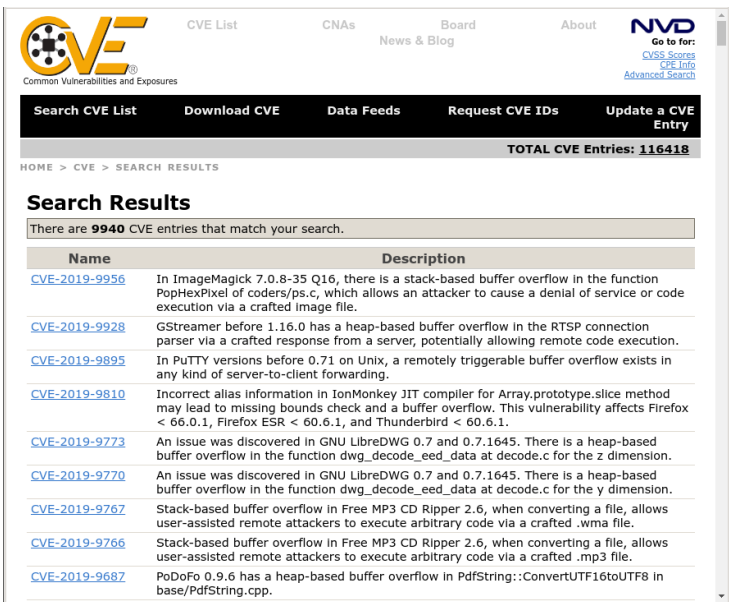
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Search for “buffer overflow” on CVE



The screenshot shows the CVE website interface. At the top left is the CVE logo with the text "Common Vulnerabilities and Exposures". Navigation links include "CVE List", "CNAs", "Board News & Blog", and "About". On the top right is the NVD logo with links for "Go to for: CVSS Scores", "CVE Info", and "Advanced Search". A black navigation bar contains buttons for "Search CVE List", "Download CVE", "Data Feeds", "Request CVE IDs", and "Update a CVE Entry". Below this bar, a grey box displays "TOTAL CVE Entries: 116418". The main content area shows the breadcrumb "HOME > CVE > SEARCH RESULTS" followed by the heading "Search Results". A message states "There are 9940 CVE entries that match your search." Below this is a table with two columns: "Name" and "Description". The table lists several CVE entries, each with a link to the full entry and a brief description of the vulnerability.

Name	Description
CVE-2019-9956	In ImageMagick 7.0.8-35 Q16, there is a stack-based buffer overflow in the function PopHexPixel of coders/ps.c, which allows an attacker to cause a denial of service or code execution via a crafted image file.
CVE-2019-9928	GStreamer before 1.16.0 has a heap-based buffer overflow in the RTSP connection parser via a crafted response from a server, potentially allowing remote code execution.
CVE-2019-9895	In PuTTY versions before 0.71 on Unix, a remotely triggerable buffer overflow exists in any kind of server-to-client forwarding.
CVE-2019-9810	Incorrect alias information in IonMonkey JIT compiler for Array.prototype.slice method may lead to missing bounds check and a buffer overflow. This vulnerability affects Firefox < 66.0.1, Firefox ESR < 60.6.1, and Thunderbird < 60.6.1.
CVE-2019-9773	An issue was discovered in GNU LibreDWG 0.7 and 0.7.1645. There is a heap-based buffer overflow in the function dwg_decode_ee_data at decode.c for the z dimension.
CVE-2019-9770	An issue was discovered in GNU LibreDWG 0.7 and 0.7.1645. There is a heap-based buffer overflow in the function dwg_decode_ee_data at decode.c for the y dimension.
CVE-2019-9767	Stack-based buffer overflow in Free MP3 CD Ripper 2.6, when converting a file, allows user-assisted remote attackers to execute arbitrary code via a crafted .wma file.
CVE-2019-9766	Stack-based buffer overflow in Free MP3 CD Ripper 2.6, when converting a file, allows user-assisted remote attackers to execute arbitrary code via a crafted .mp3 file.
CVE-2019-9687	PoDoFo 0.9.6 has a heap-based buffer overflow in PdfString::ConvertUTF16toUTF8 in base/PdfString.cpp.

- So many vulnerabilities related to buffer overflow
- Severe consequences (especially for C/C++ programs)
 - Denial of service
 - Remote code execution
 - Privilege escalation

An ever green

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Who cares about C/C++

You should! Your OS is written in C/C++. Your browser too.

Goal of this lecture

- Understand low level mechanisms of program execution
- Exploit common mistakes to deviate from standard behavior
- Craft and inject shellcodes
- Practice with `gdb` and `peda`

We work with linux x86 (32-bits)

Just because it is simpler than other (64-bits) OSes for this purpose

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- A register stores the instruction pointer (IP)
- Conditional instructions are used to break sequentiality

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- The data section contains program's data

Program execution

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Such a separation is not always checked

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A buffer overflow may replace the return address

The Morris Worm

The `finger` program

Provides information on `user@machine`

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It had a buffer overflow

- Assumed that people would rely on short names
- Allocated only 11 bytes for `user@machine` (plus null character)
- Morris provided a long string to execute a shellcode

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Why this attack was possible

- Separation of data and instructions was not checked
- `finger` ran with root privilege

- 1 Introduction
 - Context and goal
 - Overview and example
- 2 Assembly
 - Warm up
 - Computer architecture
 - Most frequent instructions
 - C/C++ calling convention
- 3 Buffer overflow and shellcode
 - Simple examples
 - Privilege escalation
 - Help yourself with peda
 - Return-to-libc and ROP
- 4 Conclusion

- 1 Introduction
 - Context and goal
 - Overview and example
- 2 Assembly
 - **Warm up**
 - Computer architecture
 - Most frequent instructions
 - C/C++ calling convention
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- 4 Conclusion

What is assembly language?

- The CPU manages arithmetical, logical, and control activities
- The CPU follows machine language instructions
- Machine language instructions are strings in $\{0, 1\}^*$
- Assembly is almost one-to-one to machine language

Why studying an assembly language?

To understand the following:

- How programs interface with OS, processor, and BIOS
- How data is represented in memory and other external devices
- How the processor accesses and executes instruction
- How instructions access and process data
- How a program accesses external devices

We are going to use an online disassembler

<https://godbolt.org/>

- Select C++ as language (on the left)
- Select x86-64 gcc 4.8.1 as compiler (on the right)
- Set `-m32` as command-line option

The screenshot shows the Compiler Explorer interface. On the left, the C++ source code is displayed in a text editor:

```
1 #include <stdio.h>
2
3 int f() {
4     int a = 1, b = 2, c = 3, d = 4;
5     //printf("hello world!");
6     return 0;
7 }
8
9 int main(int argc, char* argv[]) {
10     printf("hello world!");
11     return 0;
12 }
```

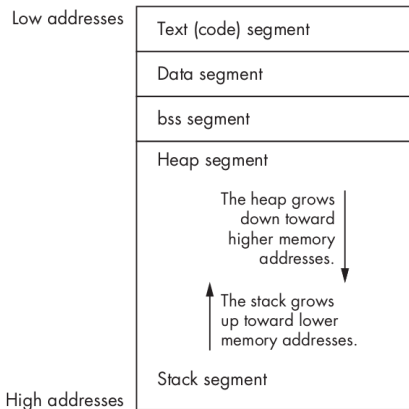
On the right, the assembly output for the `f()` function is shown:

```
1 f():
2     push    ebp
3     mov     ebp, esp
4     sub     esp, 16
5     mov     DWORD PTR [ebp-4], 1
6     mov     DWORD PTR [ebp-8], 2
7     mov     DWORD PTR [ebp-12], 3
8     mov     DWORD PTR [ebp-16], 4
9     mov     eax, 0
10    leave
11    ret
```

The interface also shows the compiler selected as `x86-64 gcc 4.8.1` and the command-line option `-m32` set.

- 1 Introduction
 - Context and goal
 - Overview and example
- 2 Assembly
 - Warm up
 - **Computer architecture**
 - Most frequent instructions
 - C/C++ calling convention
- 3 Buffer overflow and shellcode
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 - Return-to-libc and ROP
- 4 Conclusion

Memory segments

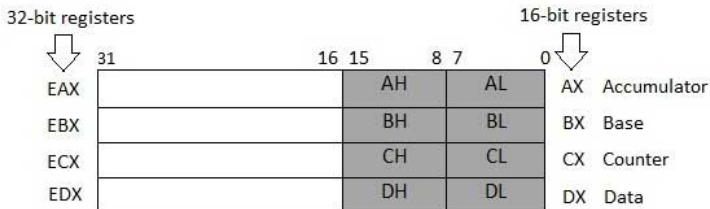


- Text: assembly code
- Data: global initialized variables
- BSS: global uninitialized variables
- Heap: dynamically allocated memory
- Stack: local (and temporary) memory

Registers of an x86 processor

- General registers
 - Data registers
 - Pointer registers
 - Index registers
- Control registers
- and others

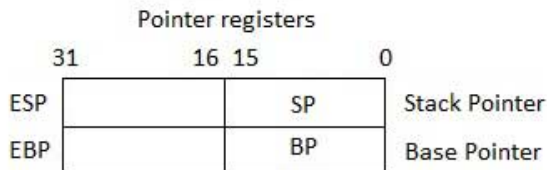
Data registers



- Four 32-bit data registers
- Used for arithmetic, logical and other operations
- Can be also used as 16-bit or 8-bit data registers

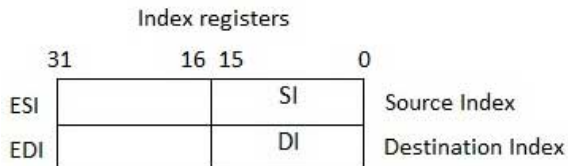
AX, BX, CX, DX use bits 0-15

Pointer registers



- Three 32-bit pointer registers
 - ESP: address of current top stack element
 - EBP: address of the stack frame
- Can be also used as 16-bit pointer registers

Index registers



- Two 32-bit index registers
- Used for addressing memory
- Can be also used as 16-bit pointer registers

Control registers

- EIP: 32-bit instruction pointer register
 - Address of the next instruction to be executed

Flag:					O	D	I	T	S	Z		A		P		C
Bit no:	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0

- EFLAGS: 32-bit flags register
 - Overflow Flag (OF): 1 if the last arith. op. overflowed
 - Direction Flag (DF): left-to-right (0) or right-to-left (1) processing of strings
 - Interrupt Flag (IF): ignore (0) or process (1) external interrupts
 - Trap Flag (TF): 1 for single-step execution (to debug)
 - Sign Flag (SF): 0 if the last arith. op. gave a positive result
 - Zero Flag (ZF): 1 if the last arith. op. gave 0
 - Auxiliary Carry Flag (AF): the carry from bit 3 to bit 4 in the last arith. op.
 - Parity Flag (PF): parity bit of the last arith. op.
 - Carry Flag (CF): the carry of the high-order bit in the last arith. op.

- 1 Introduction
 - Context and goal
 - Overview and example
- 2 Assembly
 - Warm up
 - Computer architecture
 - **Most frequent instructions**
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Arithmetic instructions

- `inc destination`
- `dec destination`
- `add destination, source`
- `sub destination, source`

At least one operand must be different from memory address

Bitwise logical operations, storing the result in `operand1`:

- `and operand1, operand2`
- `or operand1, operand2`
- `xor operand1, operand2`
- `not operand1`

Bitwise AND, just setting flags (ZF is set to 1 if the AND is 0)

- `test operand1, operand2`

Unconditional jump

- `jmp label`
Set IP to the address of the given label

```
MOV  AX, 00    ; Initializing AX to 0
MOV  BX, 00    ; Initializing BX to 0
MOV  CX, 01    ; Initializing CX to 1
L20:
ADD  AX, 01    ; Increment AX
ADD  BX, AX    ; Add AX to BX
SHL  CX, 1     ; shift left CX, this in turn doubles the CX value
JMP  L20      ; repeats the statements
```

Conditional jump

■ `j<condition> label`

Instruction	Description	Flags tested
<code>JE/JZ</code>	Jump Equal or Jump Zero	ZF
<code>JNE/JNZ</code>	Jump not Equal or Jump Not Zero	ZF
<code>JG/JNLE</code>	Jump Greater or Jump Not Less/Equal	OF, SF, ZF
<code>JGE/JNL</code>	Jump Greater/Equal or Jump Not Less	OF, SF
<code>JL/JNGE</code>	Jump Less or Jump Not Greater/Equal	OF, SF
<code>JLE/JNG</code>	Jump Less/Equal or Jump Not Greater	OF, SF, ZF

- Often preceded by `cmp operand1, operand2`
- It is like `sub`, but `operand1` is not changed
- Only flags are affected

```
INC     EDX
CMP     EDX, 10 ; Compares whether the counter has reached 10
JLE     LP1    ; If it is less than or equal to 10, then jump to LP1
```

Let's try a few examples

- Go to `https://godbolt.org/` and try common constructs
- Colors help to understand how C/C++ is compiled into assembly instructions
- Assign unique constants to variables to easily identify them
- Try `if..then`, `if..then..else`, `while`, `do..while`, `for`

- 1 Introduction
 - Context and goal
 - Overview and example
- 2 Assembly
 - Warm up
 - Computer architecture
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- 3 Buffer overflow and shellcode
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- `push operand`
- `pop address/register`
- Used for local variables
- Used to create cached copies
- Used for passing arguments to procedures

- Subroutines are identified by labels
- Subroutines are called by `call label`
 - Pushes EIP into the stack, and jumps to `label`
- Each subroutine terminates with `ret`
 - Pops an address from the stack, and jumps to it

Calling convention

- How to share subroutines?
- We must agree on some strategy to pass parameters
- Several conventions do exist
- We will consider the C/C++ convention
- Essentially, use the stack!

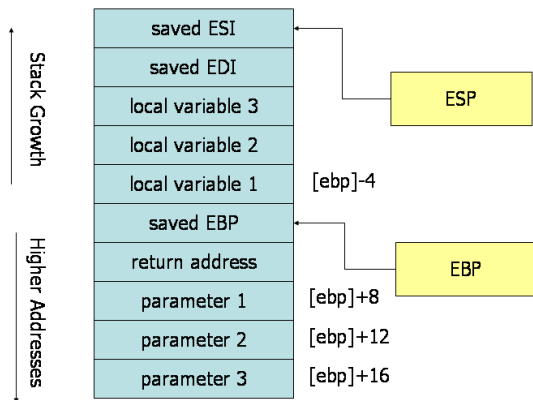
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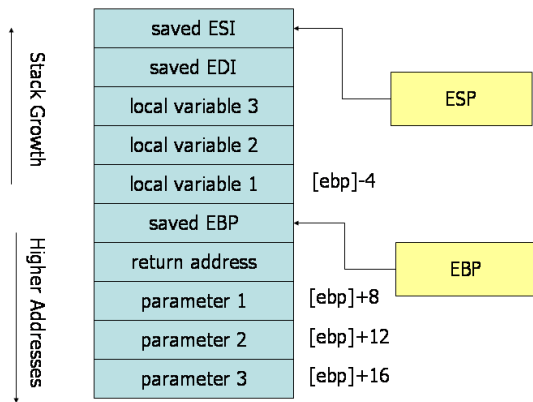
Two sets of rules

- 1 The first set is for the caller
- 2 The second set is for the callee

Caller rules

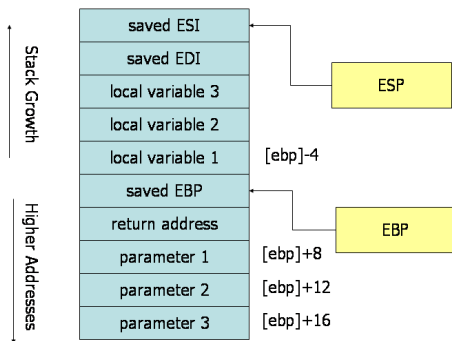


- 1 Push *caller-saved* registers: EAX, ECX, EDX
- 2 Push arguments in reverse order (allow varadics)
- 3 Use the `call` instruction (push return address, and jump)
- 4 Remove parameters from the stack (add their size to ESP)
- 5 Restore caller-saved registers (pop them from the stack)



Subroutine Prologue

- 1 Push EBP, and then copy ESP into EBP
 - All parameters are in EBP-offset
- 2 Allocate local variables in the stack
 - Subtract their size from ESP
 - All local variables are in EBP+offset
- 3 Push *callee-saved* registers: EBX, EDI, ESI



Subroutine Epilogue

- 1 Leave the return value in EAX
- 2 Restore callee-saved registers (pop them)
- 3 Deallocate local variables
 - Add their size to ESP
 - Better alternative, copy EBP into ESP
- 4 Restore the previous EBP (pop it)
- 5 Return to the caller by executing `ret`

Instruction `leave` is equivalent to

- `mov esp, ebp`
- `pop ebp`

It is a shortcut for **3** and **4** in the previous slide.

- 1 Introduction
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 - Overview and example
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 - Warm up
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- 3 Buffer overflow and shellcode**
 - Simple examples**
 - Privilege escalation**
 - Help yourself with peda**
 - Return-to-libc and ROP**
- 4 Conclusion

- 1 Introduction
 - Context and goal
 - Overview and example
- 2 Assembly
 - Warm up
 - Computer architecture
 - Most frequent instructions
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- 4 Conclusion

- Use instruction `__asm__ (<assembly-code-here>)`
- Compile with `-masm=intel`

Example

Try `find_start.c`

- Essentially, writing after the last element of an array
- Target EIP to control execution of the running program

Example

- 1 Try `buffer.c`

Overflow into the return address

- Try `overflow.c`
- Function `gets()` does not bound its argument
- Find the address of function `unlinked_code`, say `0x0804845b`
- Try the following:

```
for i in $(seq 30 50); do
    echo $i;
    python -c "print('A'*$i + '\x5b\x84\x04\x08')" | a.out;
done
```

- 1 Introduction
 - Context and goal
 - Overview and example
- 2 Assembly
 - Warm up
 - Computer architecture
 - Most frequent instructions
 - C/C++ calling convention
- 3 **Buffer overflow and shellcode**
 - Simple examples
 - **Privilege escalation**
 - Help yourself with peda
 - Return-to-libc and ROP
- 4 Conclusion

Spawn a shell in C

- Try `shell.c`
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What all this means

- Processes are associated with two user ids
 - Real UID: who started the process
 - Effective UID: for who the process acts
- Similarly, there are real and effective group ids

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What all this means

- Processes are associated with two user ids
 - Real UID: who started the process
 - Effective UID: for who the process acts
- Similarly, there are real and effective group ids
- If SUID is set, effective UID is set to the user owning the file
- If SGID is set, effective GID is set to the group owning the file
- `exec*` functions start new processes... acting for the effective user and group!

Our first shellcode

- A shellcode is a set of machine instructions
- Essentially, instructions spawning a shell
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Our first shellcode

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Problems we have to face

- Inject our shellcode in a vulnerable buffer
- Jump to the first instruction of our shellcode

- Try `victim.c`
- How to inject our shellcode?

- Try `victim.c`
- How to inject our shellcode?

The NOP Method

```
<NOPS (0x90)> <shellcode> <padding> <saved return address>
```

- We will jump in the NOP sled
- The more NOPS, the more likely the injection
- Follow the instructions

- 1 Introduction
 - Context and goal
 - Overview and example
- 2 Assembly
 - Warm up
 - Computer architecture
 - Most frequent instructions
 - C/C++ calling convention
- 3 **Buffer overflow and shellcode**
 - Simple examples
 - Privilege escalation
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 - Return-to-libc and ROP
- 4 Conclusion

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- They add pretty printing functionalities
- One of them is `peda`
- Execute `source <path to peda.py>` in `gdb`

Download `peda` from github

<https://github.com/longld/peda>

- Use `pattern create` to create a long pattern

```
gdb-peda$ pattern create 1024
' AAAAAsAABAA$AAAnACAA - AA( AADAA; AA) AAEAAaAA0AFAAbAA1AAGAacAA2AAHAAdAA3AAIAeAA4AAJAAfAA5AAKAA
gAA6AALAAhAA7AAMAA1AABAANAAjAA9AA0AAkAAPAALAAQAAMAAARAoAASAAPAAATAAQAAUAARAAVAAtAAWAAuAAXAAvAAy
AAwAAZAAXAAyAAzA%A%A$A%BA%A%nA%CA% - A%( A%DAs; A%) A%EA%A%0A%FA%bA%1A%GA%cA%2A%HA%dA%3A%IA%eA%4A
%JA%fA%5A%KA%gA%6A%LA%hA%7A%MA%iA%8A%NA%jA%9A%OA%kA%PA%LA%QA%MA%RA%oA%SA%pA%TA%QA%UA%rA%VA%tA%
WA%uA%X%A%vA%YA%wA%ZA%xA%yA%zAs%AssAsBAs$AsnAsCAs - As( AsDAs; As) AsEAsaAs0AsFAsbAs1AsGAscAs2AsHAsd
As3AsIAseAs4AsJAsfAs5AsKAsgAs6AsLAsHAs7AsMAsiAs8AsNAsjAs9AsOAskAsPaslAsQAsmAsRAsoAsSAspAsTAsqA
sUAsrAsVAsTAsWAsuAsXAsvAsYAswAsZAsxAsyAszAB%ABsABBAB$ABnABCAB - AB( ABDAB; AB) ABEABaAB0ABFABbAB1AB
GABcAB2ABHABdAB3ABIABeAB4ABJABfAB5ABKABgAB6ABLABhAB7ABMABiAB8ABNABjAB9ABOABkABPABlABQABmABRABo
ABSABpABTABqABUABrABVABtABWABuABXABvABYABwABZABxABYABzA%A%A$A$BA$$A$A$CA$ - A$( A%DAs; A$) A$EA$aA
$OA$FA$bA$1A$GA$cA$2A$HA$dA$3A$I A$eA$4A$JA$fA$5A$KA$gA$6A$LA$hA$7A$MA$iA$8A$NA$jA$9A$OA$kA$PA$
LA$QA$mA$RA$oA$SA$pA$TA$qA$UA$rA$VA$tA$WA$uA$XA$vA$YA$wA$ZA$xA$yA$ZAn%AnsAnBAn$AnnAnC'
```

- Use `pattern create` to create a long pattern

```
gdb-peda$ pattern create 1024
'AAAAsAABAA$AAnAACAA-AA(AADAA;AA)AEEAAaAA0AFaAbAA1AAGAAcAA2AAHAAdAA3AAIAeAA4AAJAAfAA5AAKAA
gAA6AALAAhAA7AAMAA1AABAANAAjAA9AA0AAkAAPAALAAQAAMAAARAoAA5AApAATAAqAAUAArAAVAAtAAWAAuAAXAAvAAy
AAwAAZAAXAAyAAzA%A%A%BA%A%NA%CA%-A%(A%DAA;A%)A%EA%aA%0A%FA%bA%1A%GA%cA%2A%HA%dA%3A%IA%eA%4A
%JA%fA%5A%KA%gA%6A%LA%hA%7A%MA%iA%8A%NA%jA%9A%OA%kA%PA%LA%QA%MA%RA%oA%SA%pA%TA%QA%UA%rA%VA%tA%
WA%uA%XA%vA%YA%wA%ZA%xA%yA%zAs%AssAsBAs$AsnAsCAs-As(AsDAs;As)AsEAsaAs0AsFAsbAs1AsGAscAs2AsHAsd
As3AsIAseAs4AsJAsfAs5AsKAsgAs6AsLAsHAs7AsMAsiAs8AsNAsjAs9AsOAskAsPAslAsQAsmAsRAsoAsSAspAsTAsqA
sUAsrAsVAsTAsWAsuAsXAsvAsYAswAsZAsxAsyAszAB%ABsABBAB$ABnABCAB-AB(ABDAB;AB)ABEABaAB0ABFABbAB1AB
GABcAB2ABHABdAB3ABIABeAB4ABJABfAB5ABKABgAB6ABLABhAB7ABMAB1AB8ABNABjAB9ABOABkABPABlABQABmABRABo
ABSABpABTABqABUABrABVABtABWABuABXABvABYABwABZABxABYABzA%A%A%$BA%A%A%$nA%CA%-A$(A%DAA;A%)A$EA$aA
$OA$FA$bA$1A$GA$cA$2A$HA$dA$3A$IA$eA$4A$JA$fA$5A$KA$gA$6A$LA$hA$7A$MA$iA$8A$NA$jA$9A$OA$kA$PA$
LA$QA$mA$RA$oA$SA$pA$TA$qA$UA$rA$VA$tA$WA$uA$XA$vA$YA$wA$ZA$xA$yA$ZAn%AnsAnBAn$AnnAnC'
```

- Crash the process using the pattern

```
Stopped reason: SIGSEGV
0x73413973 in ?? ()
gdb-peda$
```


- Use `pattern create` to create a long pattern

```
gdb-peda$ pattern create 1024
'AAAAsAABAA$AAnAACAA-AA(AADAA;AA)AEEAAaAA0AFAAbAA1AAGAAcAA2AAHAAdAA3AAIAeAA4AAJAAfAA5AAKAA
gAA6AALAAhAA7AAMAAIAABAANAAjAA9AA0AAkAAPAALAAQAAMAAARAoAA5AApAATAAqAAUAArAAVAAtAAWAAuAAXAAvAAy
AAwAAZAAXAAyAAzA%A%A%BA%A%CA%-A%(A%DA%;A%)A%EA%aA%0A%FA%bA%1A%GA%cA%2A%HA%dA%3A%IA%eA%4A
%JA%fA%5A%KA%gA%6A%LA%hA%7A%MA%iA%8A%NA%jA%9A%OA%kA%PA%LA%QA%MA%RA%oA%SA%pA%TA%QA%UA%rA%VA%tA%
WA%uA%XA%vA%YA%wA%ZA%xA%yA%zA%As%AssAsBAS$AsnAsCAS-As(AsDAs;As)AsEAsaAs0AsFAsbAs1AsGAscAs2AsHAsd
As3AsIAseAs4AsJAsfAs5AsKAsgAs6AsLAsHAs7AsMAsIA8AsNAsjAs9AsOAskAsPaslAsQAsmAsRAsoAsSAspAsTAsqA
sUAsrAsVAsTAsWAsuAsXAsvAsYAswAsZAsxAsyAszAsAB$ABsABBAB$ABnABCAB-AB(ABDAB;AB)ABEABaAB0ABFABbAB1AB
GABcAB2ABHABdAB3ABIABeAB4ABJABfAB5ABKABgAB6ABLABhAB7ABMABiAB8ABNABjAB9ABOABkABPABLABQABmABRABo
ABSABpABTABqABUABrABVABtABWABuABXABvABYABwABZABxABYABzA%A%A%$BA%A%A%$nA%CA%-A$(A%DA%;A%)A$EA%aA
$OA%FA%bA%1A%GA%cA%2A%HA%dA%3A%IA%eA%4A%JA%fA%5A%KA%gA%6A%LA%hA%7A%MA%iA%8A%NA%jA%9A%OA%kA%PA%
LA%QA%MA%RA%oA%SA%pA%TA%QA%UA%rA%VA%tA%WA%uA%XA%vA%YA%wA%ZA%xA%yA%zA%As%AssAsBAS$AsnAsCAS-As(AsDAs;As)AsEAsaAs0AsFAsbAs1AsGAscAs2AsHAsd
gdb-peda$
```

- Crash the process using the pattern

```
Stopped reason: SIGSEGV
0x73413973 in ?? ()
gdb-peda$
```

- Use `pattern offset` to compute the offset

```
gdb-peda$ pattern offset 0x73413973
1933654387 found at offset: 524
```

- Use `pattern search` to find the address of the pattern

```
gdb-peda$ pattern search
Registers contain pattern buffer:
EIP+0 found at offset: 524
EBP+0 found at offset: 520
Registers point to pattern buffer:
[EDX] --> offset 1018 - size ~6
[ESP] --> offset 528 - size ~203
[EAX] --> offset 1018 - size ~6
Pattern buffer found at:
0xffffcc10 : offset    0 - size 1024 ($sp + -0x210 [-132 dwords])
0xffffd0b6 : offset    0 - size 1024 ($sp + 0x296 [165 dwords])
References to pattern buffer found at:
0xffffcbf0 : 0xffffcc10 ($sp + -0x230 [-140 dwords])
0xffffcc00 : 0xffffcc10 ($sp + -0x220 [-136 dwords])
0xffffcc04 : 0xffffd0b6 ($sp + -0x21c [-135 dwords])
gdb-peda$
```

Build skeletons for your exploits

- Use strings of the same length
- Script your exploit as much as possible
- Follow the instructions in `peda.txt`

- 1 Introduction
 - Context and goal
 - Overview and example
- 2 Assembly
 - Warm up
 - Computer architecture
 - Most frequent instructions
 - C/C++ calling convention
- 3 Buffer overflow and shellcode**
 - Simple examples
 - Privilege escalation
 - Help yourself with peda
 - Return-to-libc and ROP**
- 4 Conclusion

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- Alternative to code injection
- Just inject return addresses (and arguments)

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- Replace the return address with the address of `system()`
- Leave 4 bytes (it is the return address of `system()`)
- Write the address of the string to execute
- Follow the instructions in `ret2libc.txt`

Return to libc (ret2libc)

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- Just inject return addresses (and arguments)

Example

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- Leave 4 bytes (it is the return address of `system()`)
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Return Oriented Programming (ROP)

Chain several calls to small instruction sets terminated by `ret`

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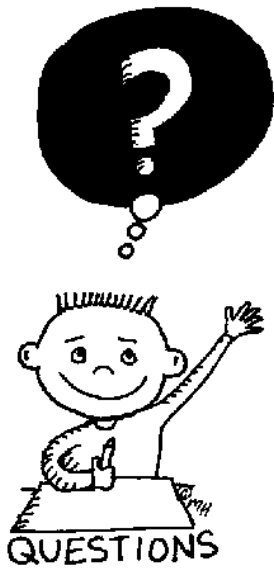
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- ASLR: Address Space Layout Randomization
 - Randomly change addresses at each execution

Take-home message

- Vulnerabilities are due to security bug
- Protection mechanisms are introduced to stop common exploit on vulnerabilities
- New exploitation techniques are developed on top of previous techniques

If you don't update your programs,
you are exposed to several known vulnerabilities



END OF THE
LECTURE